LM61
2.7V, SOT-23 or TO-92 Temperature Sensor

General Description
The LM61 is a precision integrated-circuit temperature sensor that can sense a −30˚C to +100˚C temperature range while operating from a single +2.7V supply. The LM61's output voltage is linearly proportional to Celsius (Centigrade) temperature (+10 mV/˚C) and has a DC offset of +600 mV. The offset allows reading negative temperatures without the need for a negative supply. The nominal output voltage of the LM61 ranges from +300 mV to +1600 mV for a −30˚C to +100˚C temperature range. The LM61 is calibrated to provide accuracies of ±2.0˚C at room temperature and ±3˚C over the full −25˚C to +85˚C temperature range. The LM61’s linear output, +600 mV offset, and factory calibration simplify external circuitry required in a single supply environment where reading negative temperatures is required. Because the LM61’s quiescent current is less than 125 µA, self-heating is limited to a very low 0.2˚C in still air. Shutdown capability for the LM61 is intrinsic because its inherent low power consumption allows it to be powered directly from the output of many logic gates.

Features
- Calibrated linear scale factor of +10 mV/˚C
- Rated for full −30˚C to +100˚C range
- Suitable for remote applications

Applications
- Cellular Phones
- Computers
- Power Supply Modules
- Battery Management
- FAX Machines
- Printers
- HVAC
- Disk Drives
- Appliances

Key Specifications
- Accuracy at 25˚C ±2.0 or ±3.0˚C (max)
- Accuracy for −30˚C to +100˚C ±4.0˚C (max)
- Accuracy for −25˚C to +85˚C ±3.0˚C (max)
- Temperature Slope +10 mV/˚C
- Power Supply Voltage Range +2.7V to +10V
- Current Drain @25˚C 125 µA (max)
- Nonlinearity ±0.8˚C (max)
- Output Impedance 800 Ω (max)

Typical Application

\[ V_O = (+10 \text{ mV/˚C} \times T \text{ ˚C}) + 600 \text{ mV} \]

<table>
<thead>
<tr>
<th>Temperature (T)</th>
<th>Typical ( V_O )</th>
</tr>
</thead>
<tbody>
<tr>
<td>+100˚C</td>
<td>+1600 mV</td>
</tr>
<tr>
<td>+85˚C</td>
<td>+1450 mV</td>
</tr>
<tr>
<td>+25˚C</td>
<td>+850 mV</td>
</tr>
<tr>
<td>0˚C</td>
<td>+600 mV</td>
</tr>
<tr>
<td>−25˚C</td>
<td>+350 mV</td>
</tr>
<tr>
<td>−30˚C</td>
<td>+300 mV</td>
</tr>
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</table>

FIGURE 1. Full-Range Centigrade Temperature Sensor (−30˚C to +100˚C) Operating from a Single Li-Ion Battery Cell
Connection Diagrams

SOT-23

Top View
See NS Package Number MA03B

TO-92

See NS Package Number Z03A

Ordering Information

<table>
<thead>
<tr>
<th>Order Number</th>
<th>Device Marking</th>
<th>Supplied In</th>
<th>Accuracy Over Specified Temperature Range (°C)</th>
<th>Specified Temperature Range</th>
<th>Package Type</th>
</tr>
</thead>
<tbody>
<tr>
<td>LM61BIM3 T1B</td>
<td>1000 Units on Tape and Reel</td>
<td>± 3</td>
<td>−25°C to +85°C</td>
<td>SOT-23</td>
<td></td>
</tr>
<tr>
<td>LM61BIM3X T1B</td>
<td>3000 Units on Tape and Reel</td>
<td>± 3</td>
<td>−25°C to +85°C</td>
<td>SOT-23</td>
<td></td>
</tr>
<tr>
<td>LM61CIM3 T1C</td>
<td>1000 Units on Tape and Reel</td>
<td>± 4</td>
<td>−30°C to +100°C</td>
<td>SOT-23</td>
<td></td>
</tr>
<tr>
<td>LM61CIM3X T1C</td>
<td>3000 Units on Tape and Reel</td>
<td>± 4</td>
<td>−30°C to +100°C</td>
<td>SOT-23</td>
<td></td>
</tr>
<tr>
<td>LM61BIZ</td>
<td>Bulk</td>
<td>± 3</td>
<td>−25°C to +85°C</td>
<td>TO-92</td>
<td></td>
</tr>
<tr>
<td>LM61CIZ</td>
<td>Bulk</td>
<td>± 4</td>
<td>−30°C to +100°C</td>
<td>TO-92</td>
<td></td>
</tr>
</tbody>
</table>
### Absolute Maximum Ratings (Note 1)
- **Supply Voltage**: +12V to −0.2V
- **Output Voltage**: (+V<sub>S</sub> + 0.6V) to −0.6V
- **Output Current**: 10 mA
- **Input Current at any pin (Note 2)**: 5 mA
- **Storage Temperature**: −65˚C to +150˚C
- **Maximum Junction Temperature** (T<sub>MAX</sub>): +125˚C
- **ESD Susceptibility (Note 3)**:
  - Human Body Model: 2500V
  - Machine Model: 250V
- **Lead Temperature**:
  - TO-92 Package: Soldering (10 seconds) +260˚C
  - SOT-23 Package (Note 4):
    - Vapor Phase (60 seconds) +215˚C
    - Infrared (15 seconds) +220˚C

### Operating Ratings (Note 1)
- **Specified Temperature Range**:
  - LM61C: −30˚C ≤ T<sub>A</sub> ≤ +100˚C
  - LM61B: −25˚C ≤ T<sub>A</sub> ≤ +85˚C
- **Supply Voltage Range** (+V<sub>S</sub>): +2.7V to +10V
- **Thermal Resistance, θ<sub>JA</sub>** (Note 5):
  - SOT-23: 450˚C/W
  - TO-92: 180˚C/W

### Electrical Characteristics
**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

**Note 2:** When the input voltage (V<sub>i</sub>) at any pin exceeds power supplies (V<sub>i</sub> < GND or V<sub>i</sub> > +V<sub>S</sub>), the current at that pin should be limited to 5 mA.

**Note 3:** The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

**Note 4:** See AN-450 “Surface Mounting Methods and Their Effect on Product Reliability” or the section titled “Surface Mount” found in any post 1986 National Semiconductor Linear Data Book for other methods of soldering surface mount devices.

**Note 5:** The junction to ambient thermal resistance (θ<sub>JA</sub>) is specified without a heat sink in still air.

**Note 6:** Typicals are at T<sub>J</sub> = 25˚C and represent most likely parametric norm.

**Note 7:** Limits are guaranteed to National’s AOQL (Average Outgoing Quality Level).

**Note 8:** Accuracy is defined as the error between the output voltage and +10 mV/˚C times the device’s case temperature plus 600 mV, at specified conditions of voltage, current, and temperature (expressed in ˚C).

**Note 9:** Nonlinearity is defined as the deviation of the output-voltage-versus-temperature curve from the best-fit straight line, over the device’s rated temperature range.

#### Table: Electrical Characteristics

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Conditions</th>
<th>Typical (Note 6)</th>
<th>LM61B Limits (Note 7)</th>
<th>LM61C Limits (Note 7)</th>
<th>Units (Limit)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Accuracy (Note 8)</td>
<td></td>
<td>±2.0 ±3.0</td>
<td>±3.0 ±4.0</td>
<td>°C (max)</td>
<td></td>
</tr>
<tr>
<td>Output Voltage at 0˚C</td>
<td>+600</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Nonlinearity (Note 9)</td>
<td></td>
<td>±0.6 ±0.8</td>
<td>°C (max)</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Sensor Gain (Average Slope)</td>
<td>+10</td>
<td>+9.7 +9.6</td>
<td>+10.3 +10.4</td>
<td>mV/˚C (min)</td>
<td></td>
</tr>
<tr>
<td>Output Impedance</td>
<td>+3.0V ≤ V&lt;sub&gt;S&lt;/sub&gt; ≤ +10V</td>
<td>0.8 0.8</td>
<td>2.3 2.3</td>
<td>kΩ (max)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>−30˚C ≤ T&lt;sub&gt;A&lt;/sub&gt; ≤ +85˚C, +V&lt;sub&gt;S&lt;/sub&gt; = +2.7V</td>
<td>2.3 2.3</td>
<td>5 5</td>
<td>kΩ (max)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>+85˚C ≤ T&lt;sub&gt;A&lt;/sub&gt; ≤ +100˚C, +V&lt;sub&gt;S&lt;/sub&gt; = +2.7V</td>
<td>5 5</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Line Regulation (Note 10)</td>
<td>+3.0V ≤ V&lt;sub&gt;S&lt;/sub&gt; ≤ +10V</td>
<td>±0.7 ±0.7</td>
<td>±5.7 ±5.7</td>
<td>mV/V (max)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>+2.7V ≤ V&lt;sub&gt;S&lt;/sub&gt; ≤ +3.3V</td>
<td></td>
<td></td>
<td>mV (max)</td>
<td></td>
</tr>
<tr>
<td>Quiescent Current</td>
<td>+2.7V ≤ V&lt;sub&gt;S&lt;/sub&gt; ≤ +10V</td>
<td>82 125</td>
<td>125 155</td>
<td>µA (max)</td>
<td></td>
</tr>
<tr>
<td>Change of Quiescent Current</td>
<td>±5</td>
<td></td>
<td></td>
<td>µA</td>
<td></td>
</tr>
<tr>
<td>Temperature Coefficient of</td>
<td>0.2</td>
<td></td>
<td></td>
<td>µA/˚C</td>
<td></td>
</tr>
<tr>
<td>Quiescent Current</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Long Term Stability (Note 11)</td>
<td>T&lt;sub&gt;J&lt;/sub&gt;=T&lt;sub&gt;MAX&lt;/sub&gt;=+100˚C, for 1000 hours</td>
<td>±0.2</td>
<td></td>
<td>°C</td>
<td></td>
</tr>
</tbody>
</table>
Electrical Characteristics (Continued)

Note 10: Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.

Note 11: For best long-term stability, any precision circuit will give best results if the unit is aged at a warm temperature, and/or temperature cycled for at least 46 hours before long-term life test begins. This is especially true when a small (Surface-Mount) part is wave-soldered; allow time for stress relaxation to occur. The majority of the drift will occur in the first 1000 hours at elevated temperatures. The drift after 1000 hours will not continue at the first 1000 hour rate.

Typical Performance Characteristics  The LM61 in the SOT-23 package mounted to a printed circuit board as shown in Figure 2 was used to generate the following thermal curves.
Typical Performance Characteristics

The LM61 in the SOT-23 package mounted to a printed circuit board as shown in Figure 2 was used to generate the following thermal curves. (Continued)

1.0 Mounting

The LM61 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface. The temperature that the LM61 is sensing will be within about +0.2°C of the surface temperature that LM61’s leads are attached to.

This presumes that the ambient air temperature is almost the same as the surface temperature; if the air temperature were much higher or lower than the surface temperature, the actual temperature measured would be at an intermediate temperature between the surface temperature and the air temperature.

To ensure good thermal conductivity the backside of the LM61 die is directly attached to the GND pin. The lands and traces to the LM61 will, of course, be part of the printed circuit board, which is the object whose temperature is being measured.

Alternatively, the LM61 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LM61 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. Printed-circuit coatings and varnishes such as Humiseal and epoxy paints or dips are often used to ensure that moisture cannot corrode the LM61 or its connections.

The thermal resistance junction to ambient ($\theta_{JA}$) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. For the LM61 the equation used to calculate the rise in the die temperature is as follows:

$$T_J = T_A + \theta_{JA} [ (+V_S I_Q) + (+V_S - V_O) I_L ]$$

where $I_Q$ is the quiescent current and $I_L$ is the load current on the output. Since the LM61’s junction temperature is the actual temperature being measured care should be taken to minimize the load current that the LM61 is required to drive. The table shown in Figure 3 summarizes the rise in die temperature of the LM61 without any loading with a 3.3V supply, and the thermal resistance for different conditions.
1.0 Mounting (Continued)

<table>
<thead>
<tr>
<th></th>
<th>SOT-23*</th>
<th>SOT-23**</th>
<th>TO-92*</th>
<th>TO-92***</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>no heat sink</td>
<td>small heat fin</td>
<td>no heat sink</td>
<td>small heat fin</td>
</tr>
<tr>
<td>( \theta_{JA} ) (°C/W)</td>
<td>( T_J - T_A ) (°C)</td>
<td>( \theta_{JA} ) (°C/W)</td>
<td>( T_J - T_A ) (°C)</td>
<td>( \theta_{JA} ) (°C/W)</td>
</tr>
<tr>
<td>Still air</td>
<td>450</td>
<td>260</td>
<td>180</td>
<td>140</td>
</tr>
<tr>
<td>Moving air</td>
<td>180</td>
<td>90</td>
<td>70</td>
<td></td>
</tr>
</tbody>
</table>

*Part soldered to 30 gauge wire.
**Heat sink used is ½” square printed circuit board with 2 oz. foil with part attached as shown in Figure 2.
***Part glued and leads soldered to 1” square of 1/16” printed circuit board with 2oz. foil or similar.

FIGURE 3. Temperature Rise of LM61 Due to Self-Heating and Thermal Resistance (\( \theta_{JA} \))

2.0 Capacitive Loads

The LM61 handles capacitive loading well. Without any special precautions, the LM61 can drive any capacitive load as shown in Figure 4. Over the specified temperature range the LM61 has a maximum output impedance of 5 kΩ. In an extremely noisy environment it may be necessary to add some filtering to minimize noise pickup. It is recommended that 0.1 µF be added from +V_S to GND to bypass the power supply voltage, as shown in Figure 5. In a noisy environment it may be necessary to add a capacitor from the output to ground. A 1 µF output capacitor with the 5 kΩ maximum output impedance will form a 32 Hz lowpass filter. Since the thermal time constant of the LM61 is much slower than the 5 ms time constant formed by the RC, the overall response time of the LM61 will not be significantly affected. For much larger capacitors this additional time lag will increase the overall response time of the LM61.

FIGURE 4. LM61 No Decoupling Required for Capacitive Load

FIGURE 5. LM61 with Filter for Noisy Environment

FIGURE 6. Simplified Schematic
3.0 Applications Circuits

![Circuit Diagram](image)

Figure 7. Centigrade Thermostat

\[
V_{T1} = \frac{(4.1V)R2}{R2 + R1|R3}
\]

\[
V_{T2} = \frac{(4.1V)R2}{R2|R3 + R1}
\]

4.0 Recommended Solder Pads for SOT-23 Package

![Solder Pad Diagram](image)
Physical Dimensions inches (millimeters) unless otherwise noted

SOT-23 Molded Small Outline Transistor Package (M3)
Order Number LM61BIM3, LM61BIM3X, LM61CIM3 or LM61CIM3X
NS Package Number MA03B
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