

Knowledge Processing in Control Systems

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Abstract—A real time knowledge processing procedure is proposed for rule-based systems in general and control systems applications in particular. Distinguishing features of the procedure include a mechanism for rule base compression and an inference scheme based on matrix operators. The procedure is also amenable for schedulability analysis to provide response time warranty. An application concerning supervisory group control of elevators is also included to show the usefulness of the proposed procedure.

Index Terms—Artificial intelligence, real-time computer systems, computer control, knowledge engineering, real-time rule based systems, knowledge representation.

1 INTRODUCTION

REAL-TIME knowledge-based or knowledge processing systems are playing an increasingly important role in transportation, manufacturing, control, robotic and aerospace systems. They are no longer limited to low-level control functions. Control, supervision, and monitoring of complex hierarchical systems in dynamic and sometimes unpredictable or hazardous environments are typical tasks of current manmade systems.

Several large real-time applications are required to operate in environments that are not fully structured. The lack of information and uncertainty of the environment requires the use of problem solving techniques [9]. Elevator group control is one such application [1], [3]. There are many possible situations comprised: the state of all elevators; existing calls in the building; completion of previously scheduled cars; combining new hall calls with performance criteria. There are several possible corresponding schedules and as new hall calls appear, the scheduled cars must be revised frequently. Since entering all the possible responses (schedules) into the computer is unfeasible, automating the response construction process will be required. Factory scheduling is another such application [16].

Current development in real-time artificial intelligence is driven by a need to make knowledge-based systems work in real-time [9] and a need to integrate knowledge-based approaches to handle the complexities of problem-solving behavior in control systems [2], [8].

Looney [11], [12] proposed a matrix procedure for real-time knowledge processing considering only production rules with one antecedent. His procedure however does not preserve its matrix form when several antecedents are present in a rule. Furthermore, when handling multiple antecedents, the procedure is difficult to analyze and to predict if

it could meet deadlines due to the chaining scheme adopted. This is a critical issue in real-time situations.

Alternate schemes based on a network type of representation for production rules are the RETE [4] and EUREKA [5] procedures. However, both procedures lack the predictability property, which is essential in real-time applications.

More recently, Paul et al. [13] developed an approach which integrates problem-solving methodology and architectural primitives to reduce the variance at methodology level and at problem-solving level. Using this approach they have shown that problem-solving and real-time tasks coexist within a readily analyzable framework.

The purpose of this paper is to present a real-time knowledge processing (RTKP) procedure based on conjunctive and disjunctive matrices and operators.

The proposed procedure affords the setting up of the focus of attention mechanisms and guarantees its response time. Those are important characteristics that real-time knowledge-based systems should have. Furthermore, the procedure structure is such that parallel implementation is natural. Even with conventional computer architecture the procedure can be implemented to take advantage of CPU parallel bit operations, which may be viewed as a pseudo parallel setting up. Although we focus on the procedure's propositional logic viewpoint, its extension to fuzzy logic is immediate. Certainty factors can also be easily handled.

The proposed procedure, as opposed to [11], preserves the general structure even when processing rules with multiple antecedents. It also allows serial, pseudo or fully parallel implementations and its extended version supports fuzzy and certainty factor reasoning. These features are not observed in [13]. Furthermore, it differs from both approaches because the exact number of data operations in each procedure call can be determined. Therefore, the response time can be obtained for each change observed on the monitored variables. Other distinguishing features are the "horizontal expansion" and "rule base compression" mechanisms used to define the tradeoff between memory size and response time. Moreover, a method is provided to avoid underutilization of relevant information and compu-

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tations. Actually, the procedure can be implemented using a structure that avoids irrelevant information, as far as control applications are concerned.

The paper is organized as follows. The next section presents the preliminaries and the general definitions needed. The procedure and its main features are addressed in Section 3. Methods for knowledge base analysis and manipulation are presented in Section 4. It is also shown that alternate versions of the procedure are easily obtained to explore the characteristics of the method. A tutorial example is provided in Section 5 to illustrate the idea behind the proposed procedure. Section 6 addresses a discussion about real-time knowledge based systems and the associated requirements and characteristics. It is shown that the procedure developed agrees with most of them. Next, in Section 7, an application concerning supervisory group control of elevators is described to show the usefulness of the proposed approach. This application is a simplified version of a real world application. Finally, Section 8 presents conclusions.

2 PRELIMINARIES AND GENERAL DEFINITIONS

A typical RTKP system acting as a direct digital control system is shown in Fig. 1. The RTKP module is connected to information sources and receivers. Sources may be sensors connected to a process, human users or even computer programs in large integrated systems. Receivers can be either actuators or human users or again computer programs. The main idea behind this scheme is that RTKP takes information from a system, processes this information with the knowledge stored in it, and then outputs new information to the system. Outputs are the control decisions. Fig. 2 shows an RTKP system performing supervisory control tasks. The knowledge processing task is encapsulated within a server to guarantee temporal isolation between it and conventional real time tasks [13], [15], [17].

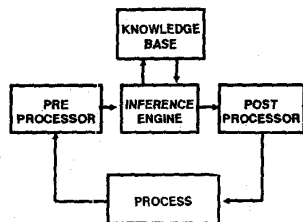


Fig. 1. Typical RTKP structure in direct digital control.

Internally, RTKP is divided into four basic parts. First is a preprocessor module responsible for the transformation of input information into the internal representation model used. This module is also responsible for any mathematical treatment that should be necessary to fit the internal representation (by making transformation of variables, for example), as well as to preprocess task dependent knowledge. The postprocessor module translates the internal representation model into output information in a format as required by the process. Between those two modules, there are the inference engine and the knowledge base. The last is the internal knowledge depository, coded in a usable for-

mat. Information provided by the preprocessor module and knowledge base is processed by the inference engine to generate the desired outputs.

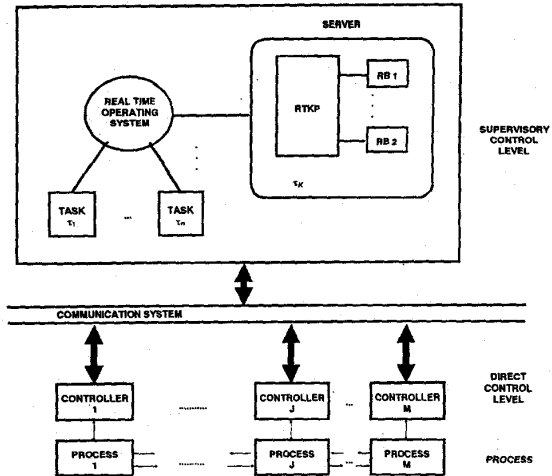


Fig. 2. RTKP in supervisory control systems.

The RTKP is defined by:

- internal representations of information provided by the preprocessor module and information to be converted by the postprocessor module,
- an internal representation of the knowledge base,
- a procedure for the inference engine.

The knowledge base is composed of two parts: rule base and fact base. A fact is the truth value associated to a particular proposition (or a term) used to store knowledge. A fact base is a set of terms, where each term has a meaning related to the process (an associated proposition). Propositions are considered within the propositional logic framework, although they could also represent fuzzy propositions. In the proposed procedure, the fact base is represented by a fact vector where each component is related to a term and contains its truth value.

The rule base can be viewed in two different representations. The first representation is for purposes of knowledge acquisition and analysis (called the virtual representation). It is presented as a collection of rules like: if <antecedent> then <consequent>, where <antecedent> represents a conjunctive association of terms and <consequent> represents a disjunctive association of terms. The second representation is a coded version of the knowledge for processing purposes.

It is relevant now to review the main requirements that real-time knowledge processing systems should fulfill [9] integration of numeric/symbolic processing, continuous operation, focus of attention mechanisms, interrupt manager services, optimum use of environment, response time warranty, temporal data processing, truth data maintenance, and the fact it dispenses explanatory modules. We do not, however, fully explore these requirements in this paper. Nevertheless, we show in Section 6 how the scheme proposed here helps in addressing such important requirements, mainly when applications are of primary concern.

3 PROCEDURE DESCRIPTION

3.1 Fact Base Codification

The real-time inference engine considers two fact bases. The first, called input fact base, has the truth values of terms evaluated by the preprocessor module. The second, called output fact base, contains truth values of the terms after the inference procedure.

Each fact base will be represented by a vector where each component is a Boolean variable. This vector has as many elements as the number of terms used in the rule base. There is a one to one relation between each term and a vector component.

DEFINITION 1 (Fact Vector):

Let E be the set of terms which represent the propositional symbols over a universe of discourse.

$$E = \{e_i \mid e_i \text{ propositional symbol}, 1 \leq i \leq n\}$$

Let $Card$ denote the cardinality of a set. For the set E :

$$Card(E) = n$$

Let x be a vector such that:

$$x = \{x_j \mid x_j \in \{0, 1\}, 1 \leq j \leq Card(E)\}$$

Since the terms of a fact base FB are a subset of E , the fact base can be represented by vector x with its components assuming the corresponding values:

$$x_k = \begin{cases} 1, & \text{if the truth value of } e_k \text{ is true} \\ 0, & \text{if not} \end{cases} \quad k = 1, \dots, n$$

The vector x represents the fact base FB and it will be called a fact vector. The fact bases will be codified as fact vectors. An input fact base is denoted by x_i , called input fact vector. The output fact base is denoted by x_o , called output fact vector.

3.2 Rule Base Codification

The rule base used by the procedure is a mapping of its virtual representation. It will be represented by two operational matrices, C (the conjunctive matrix) and D (the disjunctive matrix).

The pair of matrices C and D is called the Operational Rule Base since their structures store rules' information to be effectively used during inference. The inference procedure is derived from operational matrices, operators and the fact vector.

DEFINITION 2 (Propositional Rule):

Let E be as in definition 1.

A rule R is defined by the union of its syntactic and semantic structures.

The syntactic structure of a rule is defined as a word sequence, each being a set of characters such as:

if <antecedent> then <consequent>

where <antecedent> relates from 1 up to $(n - 1)$ elements of E , conjunctively associated by the Boolean operator \wedge ("and"),

and <consequent> corresponds to an element of E .

Let A be a subset of E such that the elements of A are members of the <antecedent> of rule R syntactic structure, that is:

$$A = \{a_i \mid a_i \in E, a_i \text{ being member of } \langle \text{antecedent} \rangle, 1 \leq i \leq k, k < n\}$$

Let Q be a singleton of E such that its element q is a consequent of rule R syntactic structure:

$$Q = \{q \mid q \in E, q \text{ is a consequent}\}$$

The semantic structure of a rule is defined by associating it to a propositional logic sentence as below:

$$\bigwedge_i (a_i) \Rightarrow q$$

where each a_i is an element of A , q is an element of Q , " \wedge " is the conjunctive logic operator, and " \Rightarrow " is the implication logic operator from the propositional logic.

DEFINITION 3 (Virtual Rule Base):

A virtual rule base VRB is a set of rules R_j , where $1 \leq j \leq m$, and m is the number of rules.

$$VRB = \{R_j \mid R_j \text{ is a rule}, 1 \leq j \leq m\}$$

DEFINITION 4 (Semantic Content):

Let E be as in Definition 1.

The semantic content of a rule R_j is defined as a function ϕ which maps all possible interpretations for E (I_E) in {false, true}, considering the logic sentence that corresponds to the semantic structure of rule R_j .

For $A_j \subset E$ and $Q_j \subset E$, if the semantic structure of R_j is:

$$(a_{j1} \wedge a_{j2} \wedge \dots \wedge a_{jk}) \Rightarrow q_j$$

then:

$$\phi(R_j, I_E): ((a_{j1} \wedge a_{j2} \wedge \dots \wedge a_{jk}) \Rightarrow q_j) \rightarrow \{\text{false, true}\}$$

Definition 4 can be extended to a rule base VRB by mapping all possible interpretations of E in {false, true}, considering the logic sentence which corresponds to the union of the semantic content in each individual rule of VRB :

$$\phi(VRB, I_E) = \phi(R_1, I_E) \vee \phi(R_2, I_E) \vee \dots \vee \phi(R_m, I_E)$$

A pair of matrices C (the conjunctive matrix) and D (the disjunctive matrix), as defined below, makes up an operational rule base which will effectively perform inference.

3.3 Matrix Structure

In matrix C , each row is associated to the list of antecedent terms of a rule in VRB , with each column representing one symbol. Thus, the C matrix has dimension $m \times n$, where m is the number of rules and n is the number of symbols in VRB .

DEFINITION 5 (C Matrix Structure):

Let E be as in Definition 1. Let VRB be a virtual rule base. Let R_i be the i th rule of VRB . Let A_i be the set of terms which are members of rule R_i antecedent.

C matrix structure is defined as:

$$C = \{c_{ij} \mid c_{ij} = f(i, j), 1 \leq i \leq m, 1 \leq j \leq n\},$$

$$f(i, j) = \begin{cases} 1, & \text{if } e_j \in A_i \\ 0, & \text{if not} \end{cases}$$

3.4 Matrix Structure

The i th row of matrix D represents rules which have the symbol i as a consequent. Therefore, the D matrix has dimension $n \times m$, where n is the number of symbols and m is the number of rules in the rule base.

DEFINITION 6 (D Matrix Structure):

Let E be as in Definition 1. Let VRB be a virtual rule base. Let R_j be the j th rule of VRB. Let Q_j be the set of terms which are members of rule R_j consequent.

The structure of matrix D is defined by:

$$D = \{d_{ij} \mid d_{ij} = g(i, j), 1 \leq i \leq n, 1 \leq j \leq m\},$$

$$g(i, j) = \begin{cases} 1, & \text{if } e_i \in Q_j \\ 0, & \text{if not} \end{cases}$$

DEFINITION 7 (Rule Base Coding):

By rule coding it is meant a modification of rule's syntactic structure, but maintaining its semantic content. Therefore, rule base coding is a modification at the rule base representation, preserving its semantic content and meaning.

Let VRB be a virtual rule base. Let C ($m \times n$) and D ($n \times m$) be, respectively, the conjunctive and disjunctive matrices. A rule base VRB codification is defined as a mapping:

$$\text{Cod}(\text{VRB}, \{C, D\}): \text{VRB} \rightarrow \{C, D\},$$

$$\phi(\text{VRB}, I_E) = \phi(\{C, D\}, I_E), \forall I_E$$

Actual implementation of the inference procedure uses CD matrices' representation for knowledge processing, since the semantic content is the same of VRB, that is, $\phi(\text{VRB}, I_E) = \phi(\{C, D\}, I_E), \forall I_E$. This equivalence is useful because at the level of man-machine interface (for knowledge acquisition and validation, for instance), the virtual representation is more appropriate. For real-time processing CD matrices representation provides a much better computing performance.

Note that, once the rule base is defined, the C and D matrices can be determined off-line.

3.5 Conjunctive Matrix Operator

The conjunctive matrix operator is an operator which does the disjunction of the rule consequents. Its definition is similar to the matrix multiplication operator, changing the algebraic product and sum by the corresponding Boolean product and sum.

DEFINITION 8 (Conjunctive Matrix Operator):

Let " \wedge " be the conjunctive Boolean scalar operator ("and"). Let " \vee " be the disjunctive Boolean scalar operator ("or"). Let M be a matrix of dimension $n \times m$ with Boolean elements m_{ij} . Let x be a Boolean vector of dimension m . Let y be a Boolean vector of dimension n .

The conjunctive matrix operator " \wedge " is defined by:

$$y = \wedge(M, x) = M \wedge x = \left\{ y_i \mid y_i = \bigwedge_j (m_{ij} \wedge x_j) \right\}$$

where

$$\bigvee_j$$

is the Boolean summation, that is

$$\bigvee_j z_j = z_1 \vee z_2 \vee \dots \vee z_m.$$

3.6 Disjunctive Matrix Operator

The disjunctive matrix operator does the conjunction of antecedent terms. It is dual to the conjunctive matrix operator, i.e. we take the same definition, changing the Boolean scalar operators " \wedge " to " \vee " and vice versa. See also [11] for similar operators.

DEFINITION 9 (Disjunctive Matrix Operator):

Let " \wedge " be the Boolean scalar conjunctive operator ("and"). Let " \vee " be the Boolean scalar disjunctive operator ("or"). Let M be a matrix of dimension $m \times n$ with Boolean elements m_{ij} . Let x be a Boolean vector of dimension n . Let y be a Boolean vector of dimension m .

We define the disjunctive matrix operator " \vee " as follows:

$$y = \vee(M, x) = M \vee x = \left\{ y_i \mid y_i = \bigvee_j (m_{ij} \vee x_j) \right\}$$

where

$$\bigwedge_j$$

is the Boolean product, that is

$$\bigwedge_j z_j = z_1 \wedge z_2 \wedge \dots \wedge z_n.$$

3.7 Inference Step

During inference, it is necessary to define a step where all rules are applied to a fact base, reassigning its truth values to the inferred ones. This scheme is defined as an inference step.

DEFINITION 10 (Inference Step):

Let C and D be the conjunctive and disjunctive matrices, respectively, which represents the operational rule base. Let \wedge

and \vee be the conjunctive and disjunctive matrix operators.

Let $\text{Neg}(A) = \bar{A}$ be the elementwise operation of Boolean negation, for matrices. Let $x + y$ be the elementwise Boolean summation for vectors. Let x_i and x_o be two fact vectors.

The inference step is defined by the following equation:

$$x_o = \text{InferenceStep}(x_i) = x_i + D \wedge (\bar{C} \vee x_i)$$

THEOREM 1 (Inference Step Theorem):

Let VRB be a virtual rule base.

Let C and D be the codification of VRB into the conjunctive and disjunctive matrices.

Let \wedge and \vee be the conjunctive and disjunctive matrix operators.

Let $+$ be the vector Boolean summation. Let $\text{Neg}(A) = \bar{A}$ be the elementwise operation of Boolean negation for matrices.

Let xi be the input fact vector and xo the output fact vector.

Starting with a generic input fact vector and applying the following formula:

$$xo = xi + D \wedge (\bar{C} \vee xi)$$

an output fact vector xo is obtained such that the truth value of all facts that can be inferred by applying rules of VRB over the facts represented by xi , including input facts truth values, are contained in xo .

PROOF. The proof of the theorem is divided in two parts. In

the first part, it is proven that the operation $(\bar{C} \vee xi)$ leads to an intermediary vector y , which holds the truth value of the antecedent of each rule. In the second part, it is proven that with the truth value of each rule antecedent and by performing the operation $D \wedge y$, we get, by "modus ponens," the truth value of all entities which are members of the consequents of rules. Therefore, one obtains all facts that can be inferred when starting with an input fact vector xi . The Boolean vector summation of this result with the original input fact vector produces an output fact vector which holds the truth value of all entities from the universe of discourse, after application of VRB.

Part 1:

By definition of disjunctive matrix operator, we have:

$$y = \bar{C} \vee x = \left\{ y_i \mid y_i = \bigvee_j (\bar{c}_{ij} \vee x_j) \right\}$$

The value of y is the Boolean product:

$$y = (\bar{c}_{i1} \vee x_1) \wedge (\bar{c}_{i2} \vee x_2) \wedge \dots \wedge (\bar{c}_{in} \vee x_n)$$

By the definition of matrix C , we have that $c_{ij} = 1$ if entity e_j belongs to the antecedent of rule R_i , being 0 if not. If $c_{ij} = 0$, then $\bar{c}_{ij} = 1$. In those cases, the term $(\bar{c}_{ij} \vee x_j)$ will always be 1, for any value of x_j . Thus, the value of those terms will not change the result of the Boolean product.

Considering a set K_i^C , of indices k such that $\bar{c}_{ik} = 1$, and a set L_i^C of indices l that $\bar{c}_{il} = 0$, we can split the Boolean product into two parts.

$$y_i = \left(\bigwedge_k (\bar{c}_{ik} \vee x_k) \right) \wedge \left(\bigwedge_l (\bar{c}_{il} \vee x_l) \right)$$

where \bar{c}_{ik} is always 1 $\forall k \in K_i^C$ and \bar{c}_{il} is always 0 $\forall l \in L_i^C$.

Since $(\bar{c}_{ik} \vee x_k)$ is always equal to 1, $\forall k \in K_i^C$, we have that

$$\left(\bigwedge_k (\bar{c}_{ik} \vee x_k) \right) = 1,$$

and therefore

$$y_i = \left(\bigwedge_l (\bar{c}_{il} \vee x_l) \right)$$

On the other hand, since \bar{c}_{il} is always equal to 0, $\forall l \in L_i^C$, we have that:

$$y_i = \bigwedge_l x_l$$

We must remember that if $\bar{c}_{il} = 0$, ($c_{il} = 1$) then e_l belongs to the antecedent of rule R_i (by definition), $\forall l \in L_i^C$. Therefore, the truth value of y_i can only be 1 if all values of x_l are 1. Since all entities e_l belong to the antecedent of rule R_i , this means that the conjunction of all x_l is equivalent to the conjunction of the truth value of entities which are members of the antecedent term of rule R_i . Then the value of y_i is equivalent to the truth value of the antecedent of rule R_i .

Part 2:

From the definition of conjunctive matrix operator:

$$xo = D \wedge y = \left\{ xo_i \mid xo_i = \bigvee_j (d_{ij} \wedge y_j) \right\}$$

From D matrix structure definition, $d_{ij} = 1$ if e_i belongs to the consequent of rule R_j and is 0 if not. Considering a set K_i^D , of indices k so that $d_{ik} = 1$, and a set L_i^D , of indices l so that $d_{il} = 0$, we can split the Boolean summation in two parts:

$$xo_i = \left(\bigvee_k (d_{ik} \wedge y_k) \right) \vee \left(\bigvee_l (d_{il} \wedge y_l) \right)$$

where d_{ik} is always 1, $\forall k \in K_i^D$ and d_{il} is always 0, $\forall l \in L_i^D$.

If d_{il} is always 0, the term $(d_{il} \wedge y_l)$ is always 0, for any value y_l , $\forall l \in L_i^D$.

Thus, we have that

$$\bigvee_l (d_{il} \wedge y_l) = 0$$

and therefore

$$xo_i = \left(\bigvee_k (d_{ik} \wedge y_k) \right)$$

Since d_{ik} is also always equal to 1, $\forall k \in K_i^D$:

$$xo_i = \bigvee_k y_k \quad (1)$$

OBS.: Modus Ponens

By "Modus Ponens" rule of inference it is meant:

$$\frac{A, A \Rightarrow B}{B},$$

i.e., if we have A , and there exists a relation that A implies B , then we can deduce B .

Remembering that if $d_{ik} = 1$, then $\forall k \in K_i^D$, $e_i \in Q_k$ (where Q_k keeps the consequent of rule k), which means that e_i is the consequent of rule R_k .

The semantic content of rule R_k can be rewritten as follows:

$$y_k \Rightarrow e_i$$

Here, for $\forall k \in K_i^D$, the expression $y_k \Rightarrow e_i$ becomes true. In other words, it is a valid formula.

By "modus ponens":

$$\frac{y_k, y_k \Rightarrow e_i}{e_i}$$

for all k belonging to K_i^D . Then e_i can be deduced from rule k if $y_k = 1$.

Since $y_k = 1$, $\forall k \in K_i^D$, we have by definition that $x_{o_i} = 1$ and therefore

$$x_{o_i} = \bigvee_k y_k$$

which is equivalent to (1).

This proves the second part of the theorem and completes the theorem proof. \square

3.8 Inference Cycle

The inference cycle is necessary to provide the chaining among rules, when it exists. An inference cycle is the successive application of inference steps, taking as input to each step the output of the last step, until it does not change anymore.

DEFINITION 11 (Inference Cycle):

The inference cycle is defined as a finite number of successive applications of inference steps over a fact vector, taking as input in each step the output of the last step. Let x_i be the input fact vector, x_o the output fact vector, and x an auxiliary fact vector. Let $(:= (a, b) \equiv a := b)$ be the vector attributive operator, defined as attributing to each element of the left side parameter, the value of the element with same index from the right side: $a[i] = b[i]$, $\forall i$. Let $(==)$ be the vector equal operator, defined as:

$$==(x, y) \equiv x == y = \begin{cases} \text{true,} & \text{if } x_i = y_i \quad i \in [1, n] \\ \text{false} & \text{if not} \end{cases}$$

The inference cycle is the following procedure:

```

procedure INFERENCE_CYCLE (var xi, xo :
  FACT_VECTOR)
var x : FACT_VECTOR; {LOCAL VARIABLE}
BEGIN
  xo := xi;
  REPEAT

```

```

  x := xo;
  xo := InferenceStep(x);
UNTIL (xo == x)

```

END

The inference cycle is the kernel of the inference engine. After an inference cycle, the output fact vector holds the result of a complete inference.

THEOREM 2 (Theorem of Inference Cycle):

Let VRB be a virtual rule base.

Let C and D be the codification of VRB through the conjunctive and disjunctive matrices.

Let \wedge and \vee be the conjunctive and disjunctive matrix operators.

Let $+$ be the Boolean summation vector operator.

Let x_i be the input fact vector and x_o be the output fact vector. Let P be the operator equivalent to an inference step:

$$P(x_i) = x_i + D \wedge (\bar{C} \vee x_i)$$

The successive application of P , taking as input of subsequent applications the last output, will result in the complete inference from a virtual rule base over a generic interpretation represented by x_i .

PROOF. By Theorem 1, the inference step is equivalent to the application of all rules of a virtual rule base VRB over a generic fact vector. The successive application of the P operator over a fact vector x_i , taking the output of each operation as input to the next one, is equivalent to the successive application of all rules from a rule base over a fact base. If there exists chaining between rules (for example, forming a chain of g levels), after applying the P operator g times, all activated rules are in fact fired. Then a complete inference over such interpretation is derived. \square

4 KNOWLEDGE BASE ANALYSIS AND MANIPULATION

Knowledge-based systems structure was conceived so to separate knowledge from its manipulation.

The knowledge manipulation performance depends heavily on how the knowledge base is represented for processing [14]. For real-time systems this is particularly important because it will run under time constraints. Therefore, a methodology for rule base analysis and manipulation is a need. The purpose of this section is to address this question.

4.1 Rule Base Analysis

The most critical part of knowledge base is the rule base, because it is the one which has more opportunities for reorganizations to achieve better performance.

It has been shown that a rule base can be described as the CD pair. What would be interesting to do is to manipulate those matrices as an attempt to get a new pair of matrices, with the same semantic content, but with reduced dimensions for increased performance.

One important question to be considered is rule chaining. Rule chaining occurs when an antecedent term of one

rule is also a consequent of another rule. Here, the application of just one inference step will not provide complete inference. To have complete inference, several inference steps are necessary.

Suppose a base with m rules and several chains, at most m steps of inference will be necessary, which is the case when all the rules are chained. In general, the number of inference steps will be less than m and equal to the length of the longest chain.

Chaining elimination seems a key for reducing and predicting processing time.

Another factor that should be considered is the need to preserve all entities used in the original rule base during all inference procedure. If we want to preserve the classic logic structure, this is necessary to maintain the coherence of the theorems that define the inference. Now, if our goal is to apply logic for control purposes, where the main issue is not the manipulation of pure mathematical logic but the effective use of knowledge for process control, we can relax the intermediate information, since it will not be relevant to the final control action. Therefore, the information to be processed can be reduced, which improves system performance in terms of processing time and memory space requirements. To deal with this aspect, we propose a method for rule base compression with deletion of intermediate information.

To address those issues, we begin by presenting a method for horizontal expansion and, afterwards, a method for rule base compression.

4.2 Rule Base Horizontal Expansion

The goal of the rule base horizontal expansion method is to eliminate chaining from the rule base, to avoid it during the on-line inference processing. The main idea is to transform a rule base with chaining into another rule base without chaining, but with the same semantic content.

The method consists in including additional rules in the rule base. The added rules are those generated by the chaining of other rules. Then, instead of applying several inference steps, only one is necessary to get the same result as if all the rules would be applied. Therefore, once we have a horizontal rule base, only one inference step is enough for complete inference. See also [11] for related results

Starting with the C and D matrices, the horizontal expansion method generates two new matrices C' and D' .

We know that, if there exists chaining it is generated by a term that is used in the consequent of the rule. First, the method swaps the rule base to verify the rule's consequents. Second, it verifies whether or not rule's consequents appear as antecedents of any rule. If yes, a new rule is generated by syllogism, and added to the new rule set.

The C matrix structure is specially interesting for doing the horizontal expansion, since the swap for a term can be done by just searching the column relative to that term to verify if c_{ij} is equal to 1 or not

DEFINITION 12 (Horizontal Rule Base):

We define a horizontal rule base as a rule base for which an inference cycle corresponds to just one inference step.

DEFINITION 13 (Horizontal Expansion Procedure):

Let $E = \{e_i\}$ be the universe of discourse.

Let VRB be a virtual rule base. Let C and D be the operational matrices that codify the virtual rule base VRB.

Let C_i be the i th row and C_j be the j th column of C matrix.

Let D_i be the i th row and D_j be the j th column of D matrix.

Let m be the number or rules of VRB, in other words, the number of rows of C matrix.

Let m' be the number of rules of the virtual rule base BR after (or during) the horizontal expansion method.

We define the horizontal expansion of a rule base by the following procedure:

```

procedure HORIZONTAL_EXPANSION(var m : dimension; var C,D : matrices)
VAR
  i,j : indices;
  m' : auxiliar_var;
BEGIN
  m' := m;
  FOR i = 1 TO m'
  BEGIN
    FOR j = 1 TO m
    BEGIN
      Find k as  $d_{ki} = 1$ ; (2)
      IF  $c_{jk} = 1$  (3)
      BEGIN
         $C_{(m'+1)i} := C_i + C_j$ ; (4)
         $c_{(m'+1)k} := 0$  (5)
         $D_{(m'+1)j} := D_j$  (6)
         $m' := m' + 1$ ;
      END
    END
  END
END

```

THEOREM 3 (Horizontal Expansion Theorem):

Let VRB be a virtual rule base, codified as its corresponding operational matrices C and D . Then, the operational matrices C' and D' generated by the horizontal expansion procedure correspond to a virtual rule base VRB', which has the same semantic content of VRB. C' and D' are a horizontal rule base.

PROOF. The horizontal expansion procedure searches all the consequents of rules (2), verifying if they are antecedents of other rules (3). If they are, it adds the antecedent terms of the rule analyzed with those of the base set (4). In the sequel, it takes off the entity which is the consequent of the rule from this set (5) and put the consequent of the base-set as the consequent of the new rule. This is equivalent to the syllogism of the two rules. Each rule added in the base-set eliminates one possible chaining that would be necessary for complete inference. This implies that, after the global search, no new chaining will be necessary. Therefore, we get a horizontal rule base. \square

It should be noted that the method increases the size of the rule base, since $m' \geq m$. This means increasing storage space, but since chaining is avoided, the processing time is predictable. There is a tradeoff between processing time and storage space to be considered. However, if a compression procedure is also applied in the horizontal rule base, we finally get a rule base that may be smaller than the original

rule base. This implies less processing time and memory requirements.

4.3 Rule Base Compression

In knowledge-based control systems, sensor information is extracted from the process and translated into facts to be processed with the rules by the inference procedure. Because of inference, control decisions are generated and translated into control signals that drive the process. The control decisions are due to a control strategy defined by a relation coded in the rule base. From the input/output point of view, sensor information is processed by a relation to generate the control decision. This means that for control purposes, only the terms associated with the sensor information and the control decisions are needed to be explicitly represented in the rule base. Moreover, it is not necessary to have in the input fact vector the corresponding terms whose truth value is not known at the beginning of inference. The output fact vector does not have to contain the information that is already included in the input fact vector. Thus, the fact vector can be partitioned into three subvectors. The first subvector, called input subvector, has as components those terms associated with the sensor input. The intermediate subvector has as its components those terms that are neither associated with sensor input nor associated with control decisions. The output subvector has as components those terms associated with the control decisions.

From the discussion above we may conclude that if an efficient scheme is available to recode and to preserve the semantic content of a rule base with those characteristics, not only the size of the rule base would be diminished (which means less storage space) but also the processing time would decrease and the predictability increase. The rule base compression method provides such a recoding scheme. After horizontal expansion, the rule base compression method first discards the intermediate terms from the input and output fact vectors. The corresponding C matrix columns and D matrix rows are also discarded. Next, the terms of the output subvector are eliminated from the input fact vector, and the terms of the input subvector are eliminated from the output fact vector. The C matrix columns corresponding to the terms of the output subvector and the D matrix rows corresponding to the terms of the input subvectors are deleted. We then finally get a compressed rule base, which recodes the original rule base and preserves its meaning. With this recoding procedure, an inference step provides complete inference (no cycles are necessary) since the inference procedure becomes:

$$x'_o = D'' \wedge (\bar{C}'' \vee x'_j) \quad (7)$$

DEFINITION 14 (Rule Base Compression):

Let VRB be a virtual rule base. Let VRB' be a horizontal rule base, generated by horizontal expansion of virtual rule base VRB . Let C and D be the matrices which codify horizontal virtual rule base VRB' .

Let E be the universe of discourse.

Let A_j be the set of entities which are members of the antecedent term of rule R_j . Let A be the set which is the result of the

union of all sets A_j , i.e., the set of all entities which are used as antecedent terms. Let Q_j be the set of entities which are members of the consequent term of rule R_j . Let Q be the set generated by the union of all sets Q_j , i.e., the set of all entities which were used as rules consequents.

Let N be the set of input entities, i.e., the set of entities which are used only as antecedent terms of VRB' .

$$N = \{n_i \mid n_i \in A, n_i \notin Q, 1 \leq i \leq \text{Card}(A - (A \cap Q))\} \quad (8)$$

Let I be the set of intermediate entities, i.e., the set of entities which are both used in antecedent and consequent terms of the VRB' rules.

$$I = \{i_k \mid i_k \in A, i_k \in Q, 1 \leq k \leq \text{Card}(A \cap Q)\} \quad (9)$$

Let S be the set of output entities, i.e., the set of entities used as consequent terms of the VRB' rules.

$$S = \{s_l \mid s_l \in Q, s_l \notin A, 1 \leq l \leq \text{Card}(Q - (A \cap Q))\} \quad (10)$$

Thus $E = N \cup I \cup S$.

Let x_i be the input fact vector and x_o the output fact vector.

We define the compression procedure by the following:

```

procedure COMPRESSION (var xi, xo :
  fact_vectors; var C,D : matrices)
BEGIN
  Discard all  $e_i \in I$  from  $x_i$  and  $x_o$ . (11)
  Discard from  $C$  all rows  $C_{i*}$  as
     $\exists j (e_j \in I \wedge c_{ij} = 1)$  (12)
  Discard from  $D$  all related columns  $D_{*j}$  (13).
  Discard from  $C$  all columns  $C_{*j}$  as  $e_j \in I$ . (14)
  Discard from  $D$  all columns  $D_{*j}$  as
     $\exists i (e_i \in I \wedge d_{ij} = 1)$  (15).
  Discard from  $C$  all related rows  $C_{j*}$ . (16)
  Discard from  $D$  all rows  $D_{i*}$  as  $e_i \in I$  (17)
  Discard all  $e_k \in S$  from  $x_i$ . (18)
  Discard all  $e_l \in N$  from  $x_o$ . (19)
  Discard from  $C$  all columns  $C_{*j}$  as  $e_j \in S$ 
  (20)
  Discard from  $D$  all rows  $D_{i*}$  as  $e_i \in N$ .
  (21)
END

```

Discarding an element from a fact vector, means the suppression of its corresponding components from the vector.

Let x be a fact vector:

$$x = \{x_1, x_2, \dots, x_{p-1}, x_p, x_{p+1}, \dots, x_n\}$$

To discard term e_p , represented by x_p , from vector x corresponds to create a vector

$$x' = \{x_1, x_2, \dots, x_{p-1}, x_{p+1}, \dots, x_n\}$$

where the x' dimension is the dimension of x decreased by 1.

To discard a row or a column from a matrix, corresponds to suppress a row or a column.

It must be observed that the compression procedure changes the indices associated with each term. Therefore, the related table of symbols must also be modified accordingly.

THEOREM 4 (Compression Theorem):

Let VRB be a virtual rule base.

Let VRB' be the horizontal expansion of VRB .

Let C' and D' be the operational matrices which codify VRB' .

Let N be the set of input entities, I be the set of intermediate entities, and S be the set of output entities.

Let xi be the input fact vector.

Let xo be the output fact vector.

Let xi' be the input fact vector modified by compression (dimension of N).

Let xo' be the output fact vector modified by compression (dimension of S).

Let $Comp(C', D', xi, xo, C'', D'', xi', xf')$ be the compression procedure which transforms VRB' into VRB'' as in Definition 14.

Let C'' and D'' be the operational matrices which codify compressed rule base VRB'' .

The truth value held by each element of xo' through the modified inference procedure, using C'' and D''

$$xo' = D'' \wedge (\overline{C''} \vee xi')$$

is the same as those that would be assigned to the related terms of xo , using the procedure based on C and D matrices and input fact vector xi .

PROOF After analyzing the compression procedure, we see that in (11), we discard the intermediate entities from operational entities. In (12) and (13), we discard all rules that had in its antecedent term an intermediate entity. In (14) we discard from matrix C the dimension related to the intermediate entities as they will not be necessary and make the necessary adjustments. In (15) and (16), we discard the rules that have in its consequent an intermediate entity. In (17) we discard the dimension of matrix D related to the intermediate entities, making the necessary dimension adjustments.

Those steps are equivalent to the elimination of intermediate entities from fact vectors and matrices. This is the same as defining a new system, where the universe of discourse is reduced to $(N \cup S)$ and the rules are similar to VRB' , except those that use an intermediate term. This will be an intermediate compression. Only those entities which are members of N can be assigned a value different from 0 (xi' represents the terms which are members of N). Since only the rules that use intermediate entities were removed (and these rules could never be fired as the truth value of intermediate terms always starts with 0, by definition), the truth value assigned by the modified procedure to C' and D' , will be the same as the conventional procedure in C and D . In (18) we discard from input fact vector, the entities of output subvector, and in (19) we discard from output fact vector, the entities of input subvector. In (20) we adjust the columns of C and in (21) we adjust the rows of D for the new representation.

Since the values of xi are equivalent to the corresponding ones in xi' and the others are 0, nothing is modified by the inference process, and the results provided by (7) are the same as those given by the procedure in Definition 10.

Discarding of input subvector terms from output fact vector does not modify inference either because they are not used in xo' .

5 EXAMPLE

A simple example is now provided to illustrate the main features of the proposed procedure.

Let us consider the following rule base:

if e_1 and e_2 then e_3 ; if e_3 and e_4 then e_5 ; if e_1 and e_6 then e_5 ;
if e_2 and e_6 then e_7 ; if e_5 and e_6 then e_7 ; if e_2 and e_4 then e_8

The encoding shown in Table 1 is defined. Then, the resulting C and D matrices are given in Table 2. After executing the Horizontal Expansion Procedure, the matrices C' and D' generated are as in Table 3. Executing the compression procedure, we get new symbol tables to represent input fact vector and output fact vector. The compressed matrices are shown in Table 4 and the new symbol tables are in Table 5.

TABLE 1
CODING SCHEME FOR RULE BASE EXAMPLE

| SYMBOL | INDEX | ANTEC. | CONS. | TYPE |
|--------|-------|--------|-------|------|
| e_1 | 1 | Y | N | N |
| e_2 | 2 | Y | N | N |
| e_3 | 3 | Y | Y | I |
| e_4 | 4 | Y | N | N |
| e_5 | 5 | Y | Y | I |
| e_6 | 6 | Y | N | N |
| e_7 | 7 | N | Y | S |
| e_8 | 8 | N | Y | S |

TABLE 2
CD MATRICES FOR EXAMPLE

$$C = \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & 1 & 0 & 1 & 0 & 0 & 0 & 0 \end{bmatrix} \quad D = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 1 \end{bmatrix}$$

TABLE 3
CD MATRICES AFTER HORIZONTAL EXPANSION

$$C' = \begin{bmatrix} 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & 1 & 0 & 1 & 0 & 0 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 1 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 1 & 0 & 1 & 0 & 0 \end{bmatrix} \quad D' = \begin{bmatrix} 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \end{bmatrix}$$

TABLE 4
CD MATRICES AFTER COMPRESSION

$$C'' = \begin{bmatrix} 0 & 1 & 0 & 1 \\ 0 & 1 & 1 & 0 \\ 1 & 0 & 0 & 1 \\ 1 & 1 & 1 & 1 \end{bmatrix} \quad D'' = \begin{bmatrix} 1 & 0 & 1 & 1 \\ 0 & 1 & 0 & 0 \end{bmatrix}$$

TABLE 5
CODING SCHEME AFTER COMPRESSION

| INPUT FACT VECTOR | | OUTPUT FACT VECTOR | |
|-------------------|-------|--------------------|-------|
| SYMBOL | INDEX | SYMBOL | INDEX |
| e_1 | 1 | e_1 | 1 |
| e_2 | 2 | e_2 | 2 |
| e_3 | 3 | | |
| e_4 | 4 | | |

Assume that the signals collected from process were pre-processed, resulting in the data given by Table 6.

TABLE 6
DATA COLLECTED FROM PROCESS

| DATA COLLECTED | |
|----------------|-------------|
| PROPOSITION | TRUTH VALUE |
| e_1 | FALSE |
| e_2 | TRUE |
| e_3 | FALSE |
| e_4 | TRUE |

Then the input fact vector is:

$$xi' = [0 \ 1 \ 0 \ 1]$$

The result provided by the inference is:

$$xo' = [1 \ 0]$$

The control action corresponding to e_7 can be executed by post-processing. The decision which corresponds to e_8 will not be executed.

6 REAL-TIME REQUIREMENTS ANALYSIS

Recalling the main requirements for real-time knowledge processing reviewed in the second section, the following can be stated.

First, an efficient numeric/symbolic processing integration is required. In the scheme proposed this is achieved by introducing the preprocessor module. Therefore, the control engineer and software designers may use any appropriate algorithm for information processing. The only constraint is to provide process information coded in the vector format to configure the fact base to be used for inference.

Regarding the focus of attention, the proposed procedure can easily consider structured rule bases, which is a mechanism to fulfill such a requirement. Here, each rule base can be coded into distinct *C* and *D* matrices. A base of meta-rules, which can be coded and operated by the same scheme, may be executed periodically to decide which rule base should be processed, depending on the process state. Setting up this or similar alternate strategies is quite simple because to do context switching, only a pointer that refers to the appropriate matrices should be modified accordingly. This scheme provides a technique for the proposed procedure to remain effective for systems requiring a very large number of rules.

As far as the requirement of optimum use of environment is concerned, due to its structure, the inference procedure proposed can be implemented by pairwise bit processing, using standard instructions of programming languages. This affords pseudoparallel implementation. Full parallel implementation is also readily achieved if the target machine has a

parallel architecture. The mechanisms for rule base compression also allow an efficient use of memory.

Response time can be assured because, as the procedure is a set of Boolean operations, the time to perform inference is easily obtained from the time needed to execute a set of primitive instructions. For instance, assuming that the procedure uses assignment, addition, comparison and increment as primitive instructions, and the corresponding execution time is *A*, *S*, *C*, and *I*, respectively, with *m* as the number of rules and *n'* as the average number of symbols per rule, procedure implementation could have the following coding scheme [6]:

```

for (i=0; i < m; i++)           (A + m.C + m.I)
{y = 1;                         (A)
 a = CL[i][0];                 (S + 2.A)
 for (j=1; j <= a&&xy == 1; j++) (A + 2.n'.C + n'.I)
   (b = x[CL[i][j]]           (3.S + 3.A)
    y = y ^ b;                 (C+A)
   )
 b =&x[DV [i]];                (2.S + A)
 *b = *b v y;                  (C+A)
}
    
```

where an account of each code line execution time is included on the right. The total execution time is such that

$$C_f(m, n') \leq A + 2.mC + m.I + 6.m.A + 3.m.S + 3.m.n'.C + m.n'.I + m.n'.4.A + m.n'.3.S.$$

Given that each instruction time can be expressed as an integer multiple of a period *T* (for example, the clock rate), the inequality above may be rewritten as

$$C_f(m, n') \leq k_1.T + k_2.m.T + k_3.m.n'.T = C_{fu}(m, n')$$

and the procedure order is $O(m, n')$, as shown by Fig. 3a and 3b. Note that $C_{fu}(m, n')$ is an upper bound for $C_f(m, n')$.

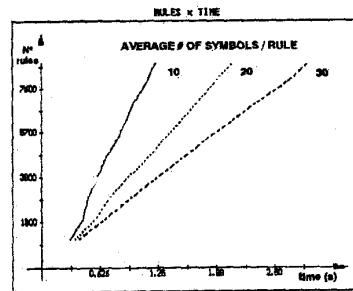


Fig. 3a. RTKP procedure time performance: rules x time.

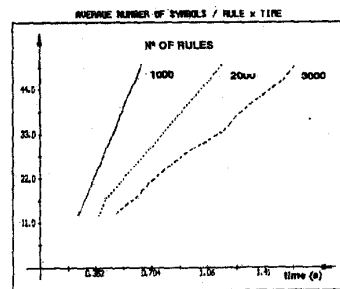


Fig. 3b. RTKP procedure time performance: symbols x time.

If preprocessing and post-processing execution times are C_i and C_o , respectively, RTKP execution time C_s is such that

$$C_s(m, n') \leq C_i + C_{fu}(m, n') + C_o = C_{su}(m, n')$$

$C_{su}(m, n')$ being an upper bound for $C_s(m, n')$.

Typical real-time input and output tasks have little to no variance associated with their execution time because there are generally no data dependencies which can cause the execution time to vary. Then, for given m and n' , the RTKP procedure is simply processed in a uniform, deterministic fashion in direct digital control applications, provided that $C_s(m, n')$ is consistent with process dynamics.

In structured knowledge bases, if $C_{s_i}(m_i, n'_i)$ is a set as the upper bound of the execution time for processing the m_i rules with n'_i symbols per rule of the i th knowledge base, the worst-case execution time $C_s(m, n')$ is

$$C_s(m, n') = \max_i \{C_{s_i}(m_i, n'_i)\}$$

The RTKP procedure can also be used in embedded real-time supervisory control applications. In these cases, it must coexist with other real-time tasks on a common computing platform. The server concept introduced by the real-time scheduling researchers is directly applied here. This approach has also been used previously [13], but was based on different grounds. Servers have been developed [15], [17] to provide highly responsive aperiodic performance over periodic, hard deadline environments. With this approach schedulability can be explicitly evaluated, that is, the level of resource utilization, attainable before a deadline is missed, can be evaluated and guaranteed.

Generally, the priority assigned to the server depends on its response time requirements. For instance, in supervisory control of elevator systems to be addressed in the next section, the knowledge processing requirements are periodic, with a period T_s of 0.5s. Since the conventional real-time tasks are also periodic, the Rate Monotonic Scheduling Theory [10] can readily be used to evaluate schedulability of the task set. In essence, this theory ensures that since the CPU utilization of all tasks lies below a certain bound and appropriate scheduling algorithms are used, all tasks will meet their deadlines without the programmer knowing exactly when any given task will be running. Even if a transient overload occurs, a fixed subset of critical tasks will still meet their deadline since their CPU utilization lie within the appropriate bounds.

Given a set of independent periodic tasks, the rate monotonic scheduling algorithm gives each task a fixed priority and assigns higher priorities to tasks with shorter periods. A sufficient worst case condition that characterize schedulability of a task set under the rate monotone algorithm is provided by [10].

$$\frac{C_1}{T_1} + \dots + \frac{C_n}{T_n} \leq n(2^{1/n} - 1) = U(n)$$

where C_i and T_i are the execution time and period of task τ_i , respectively, and one task index is assigned to the server.

To check if a set of given tasks with greater utilization than the bound provided above can meet its deadlines the

critical zone theorem [10] should be considered. The critical zone theorem states that, for a set of independent periodic tasks, if each task meets its first deadline when all tasks are started simultaneously, then the deadlines will always be met for any combination of start-times. An equivalent mathematical test which can be calculated by an exact-case analysis[15] is:

$$\forall i, 1 \leq i \leq n,$$

$$\min_{(k,l) \in R_i} \sum_{j=1}^i C_j \frac{1}{T_k} \left\lceil \frac{lT_k}{T_j} \right\rceil \leq 1$$

where C_j and T_j are the execution time and period of task τ_j , respectively, and

$$R_i = \{(k, l) \mid 1 \leq k \leq i, l = 1, \dots, \lfloor T_i/T_k \rfloor\}$$

and one task index corresponds to the server.

This theorem provide the basis for an exact schedulability test for sets of independent periodic tasks under the rate monotonic algorithm. The theorem requires checking the scheduling points for a task. The scheduling points of a task τ are τ 's first deadline and the ends of periods of high priority tasks before τ 's first deadline.

Summing up, since the worst case execution time of the RTKP procedure can be easily determined, and assuming that other task execution times are given, an analytical treatment to answer whether the server capacity is sufficient to meet the response time requirement is readily provided by the monotone rate scheduling theory. The rate monotone algorithm has been proven to be the optimal fixed-priority scheduling algorithm for periodic tasks.

Temporal data processing, although not natural within the theoretical framework considered in this paper, may be handled by either preprocessing or post-processing modules, since they can include procedures to store current and past process states or to use models to get state estimations. Truth data maintenance, although not addressed in this paper, could eventually be treated by the pre and post processing modules. Therefore, the procedure proposed here is in close agreement with the basic requirements that a real-time knowledge processing system should fulfill.

Beyond the points above, real-time knowledge based systems are often viewed as computer programs, whose implementations, inevitably, make tradeoffs between five sources [16]: processing power, response time, data space, inattention and degradation. The properties of the developed procedure clearly provide guidelines to establish the desired tradeoffs.

7 AN APPLICATION EXAMPLE: GROUP SUPERVISORY CONTROL OF ELEVATOR SYSTEMS

In traffic control of elevator systems two different control problems must be solved by a corresponding two level control hierarchy. The lower level task is to command each elevator to move up or down, to stop or start, and to open and close the door. The higher level coordinates the movement of a group of elevators through a set of logical rules crafted

to improve the system's performance. This problem is solved by means of a group supervisory control system with the aid of a group supervisory control strategy (the set of rules defining the control policy).

The main requirements of a group control system in serving both car and hall calls should be: to provide even service to every floor in a building; to minimize the time spent by passengers waiting for service, to minimize the time spent by passengers to move from one floor to another, and to serve as many passengers as possible in a given time [3]. Due to the random nature of call times, call locations and the destination of passengers, problems are encountered in attempting to achieve the above requirements. Therefore, the control strategy must be able to follow changes in passenger demands, handling different traffic patterns and adapting itself to the traffic conditions.

A practical method widely used in group supervisory control systems consists in allocating cars to serve the building hall calls. Usually only new calls are allocated, remaining fixed once made. This method is known as call allocation strategy [3]. In car allocation, however, constraints must also be considered. For instance, a car may not pass a floor at which a passenger wishes to alight, a car may not reverse its direction of travel while carrying passengers, a hall call cannot be served by a car going in the reverse direction.

In what follows, the knowledge processing procedure developed in this paper is used to implement a knowledge based group supervisory control of elevator systems. This knowledge processing approach is particularly useful because of the following:

- call allocation strategies are frequently expressed by a set of rules provided by elevator designer experts;
- different sets of rules can be easily grouped into a structured knowledge base to cover such traffic conditions as up-peak, down-peak, heavy sector demand, heavy floor demand, balanced traffic, off-peak, etc.;
- logical constraints can be easily included into the knowledge base and equally processed;
- meta-rules can be easily developed and incorporated to choose the most suited car allocation rules for a given traffic pattern;
- it predictably responds to system events.

Production rules are particularly appropriate to derive both, allocation strategies and constraints. The adaptation of the allocation strategy to the traffic condition is handled by the focus of attention mechanism, using meta-rules for traffic pattern characterization and rule base selection. Data acquisition and vector format information coding functions are performed by the preprocessor. Actual call allocation decisions and control signals, generated after inference, are computed by the post-processor.

The supervisory group control simulation example presented below, which corresponds to the supervisory control level of Fig. 2, is a simplified version of a real world system described in [7]. Table 7 presents the data used for simulation purposes. Examples of car allocation rules can be found in Table 8.

TABLE 7
ELEVATOR SYSTEM CHARACTERISTICS

| | |
|----------------------|-------------------|
| Number of floors | 7 |
| Number of elevators | 5 |
| Elevator capacity | 6 passengers each |
| Elevators Velocity | 3 m/s |
| Inter-Floor distance | 3 m |
| Door opening time | 2 s |

| | |
|-----------------------|-----------------------------|
| Door holding time | 2 s |
| Door closing time | 2 s |
| Total simulation time | 550 s (9 min and 10 s) |
| Traffic conditions | up to 34 pass/min (on peak) |
| Traffic Patterns | off-peak |

TABLE 8
EXAMPLES OF CAR ALLOCATION RULES

| | |
|---|---|
| <pre>IF elevator[E]_has_direction THEN elevator[E]_has_target IF elevator[E]_has_no_direction THEN elevator[E]_has_no_target IF elevator[E]_has_target AND place[I]_in_field AND place[I]_has_call AND call_on_place[I]_has_same_direction_elevator[E] AND call_on_place[I]_not_allocated AND elevator[E]_with_allocation_undefined THEN Allocate_Call(E,I); AND elevator[E]_with_allocation_undefined = FALSE;</pre> | <pre>IF elevator[E]_has_no_target AND place[I]_up_in_field AND place[I]_up_has_call AND call_on_place[I]_up_not_allocated AND elevator[E]_with_allocation_undefined THEN Allocate_Call(E,I); AND elevator[E]_with_allocation_undefined = FALSE; IF elevator[E]_has_no_target AND place[I]_down_in_field AND place[I]_down_has_call AND call_on_place[I]_down_not_allocated AND elevator[E]_with_allocation_undefined THEN Allocate_Call(E,I); AND elevator[E]_with_allocation_undefined = FALSE;</pre> |
|---|---|

Here, it will be demonstrated how to assign priority to the server and how to solve for its maximum capacity, consistently with the real-time tasks scheduling requirements. The example has a structured rule base for which

$$C_s(m, n') = 0.3 s$$

corresponding to $m = 500$ (rules) and $n' = 20$ (average number of symbols/rule).

The priority assigned to the server is a function of its response time requirements. In the group supervisory control application the server processing requirements are periodic with a period $T_s = 0.5$ s. Since the conventional real-time tasks are also periodic with periods summarized in Table 9, the rate monotone scheduling algorithm is readily applied to evaluate the schedulability of the task set. A tight bound can be determined by the exact-case analysis formula, which yields a maximum C_s of 0.35 which corresponds to a maximum utilization of the server of $U_s = C_s/T_s = 70\%$. Adding this to the utilization of the other real-time tasks, a total schedulable utilization of 96% is provided.

Therefore, the set of tasks of Table 9 is schedulable because the runtime of the server is 0.3 s. This can also be checked noting that, in the example, the sufficient condition

$$\frac{C_1}{T_1} + \frac{C_2}{T_2} + \frac{C_3}{T_3} = 0.87 \geq (2^{1/3} - 1) = 0.78 = U(3)$$

does not hold and then the exact-case analysis must be performed. The scheduling points are 0.5/1.0/1.5/2.0/2.5/3.0, respectively. For the fourth scheduling point we have:

$$4C_1 + C_2 + C_3 = 1.2 + 0.2 + 0.5 = 1.9 < 2.0$$

which means that the critical zone theorem holds. The actual schedulable utilization of 86% is achieved with the server runtime of 0.3 s.

TABLE 9
EXECUTION CHARACTERISTICS OF THE RUN-TIME TASK SET

| TASK | TASK DATA | | RATE MONOTONIC THEORY | |
|---------------------|--------------|----------------|-----------------------|----------|
| | Period T_i | Run-time C_i | Utilization U_i | Priority |
| Traffic Monitoring | 2.0 s | 0.2 s | 0.1 | 2 |
| User Interface | 3.0 s | 0.5 s | 0.16 | 3 |
| Supervisory Control | 0.5 s | 0.3 s | 0.6 | 1 |
| TOTAL | | | 0.86 | |

Additional results are provided by Figs. 4 and 5, which show the elevator group space x time diagrams, and the average passenger waiting time given by the evaluation of waiting time for each call.

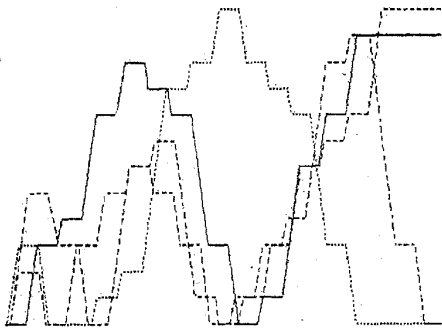


Fig. 4. Space x time—R.B. strategy.

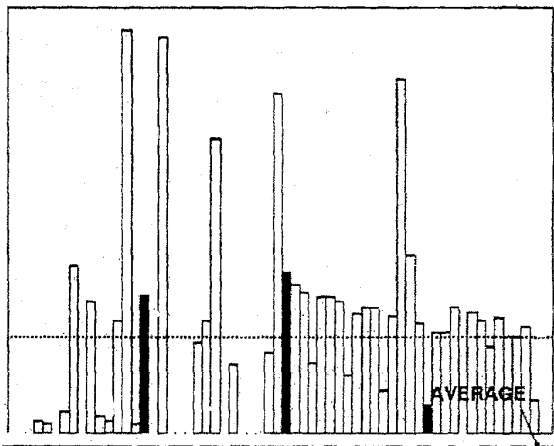


Fig. 5. Wait time x call—R.B. strategy.

Figs. 6 and 7 show the same system under control of a conventional, fixed car allocation strategy. As it is shown, the intelligent group supervisory control provides a very attractive scheme for car allocation under time varying traffic.

8 CONCLUSIONS

In this paper, a knowledge processing procedure, specially tailored for a class of real-time knowledge-based control applications, has been developed. Its main characteristics are in providing a processing scheme that closely agrees

with the basic requirements for real-time knowledge-based systems. It also includes possibilities for rule base manipulation allowing important tradeoffs between storage space, processing time and environment utilization which are fundamental for implementing real-world cases.

An application concerning supervisory, group control of elevators was also included to illustrate the procedure potential.

Currently, the approach developed is being extended to consider its use within the framework of first order predicate logic, fuzzy logic, and threshold reasoning. Important additional issues such as truth data and knowledge maintenance in real time environment are also open questions. We hope to address those items in a future paper.

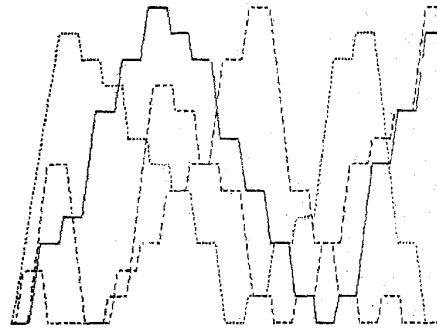


Fig. 6. Space x time—Conventional strategy.

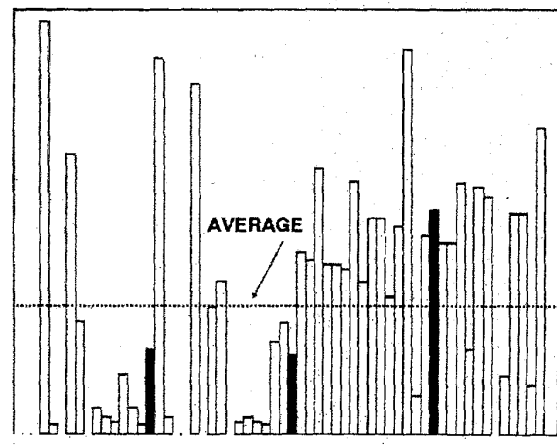


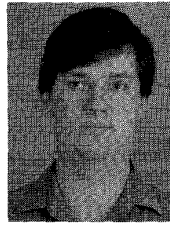
Fig. 7. Wait time x call—Conventional strategy.

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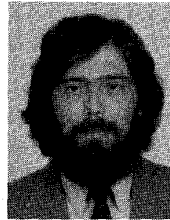
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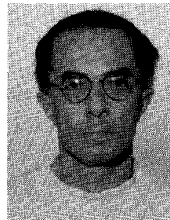
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